

- Fig 1

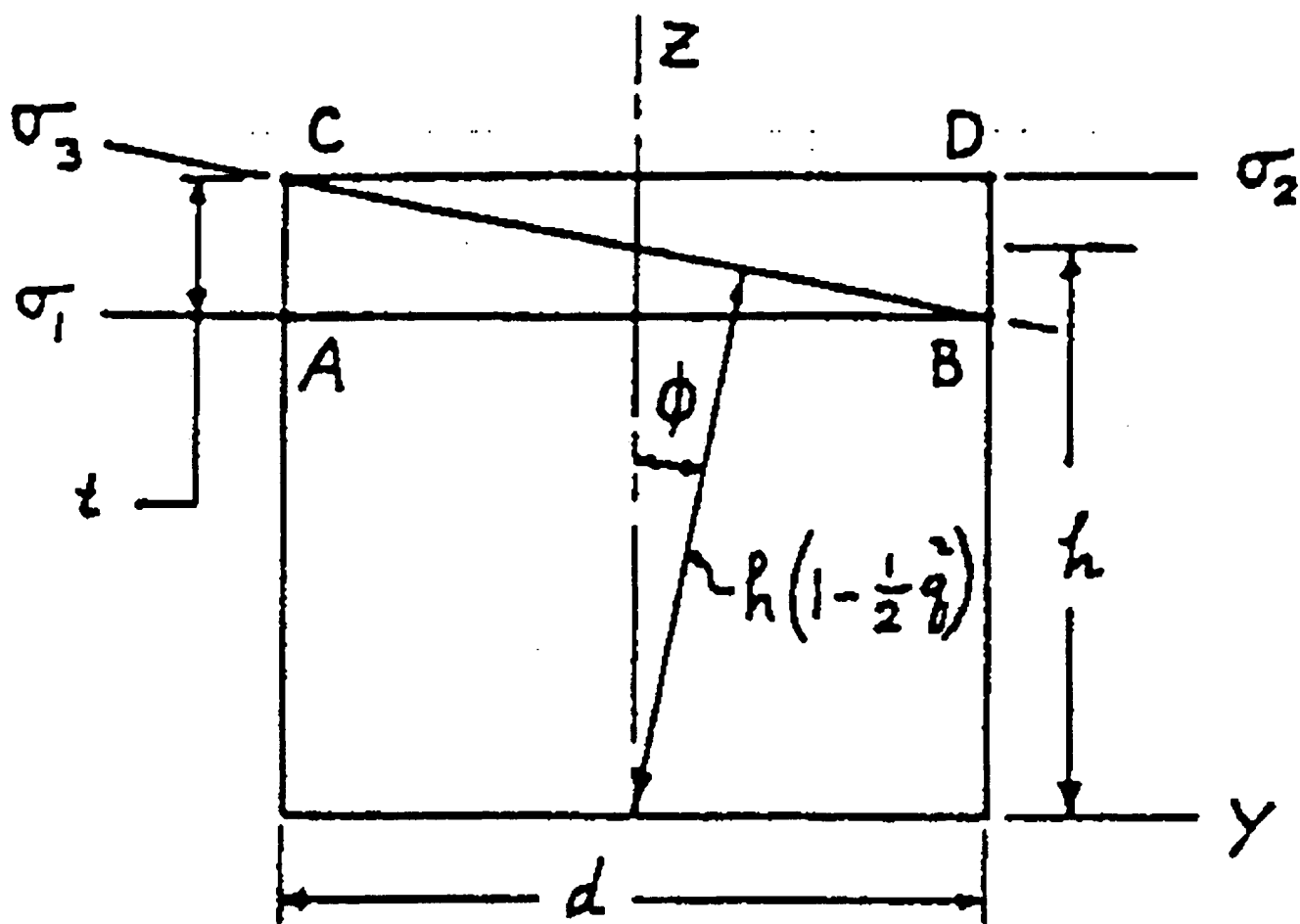


Fig 2

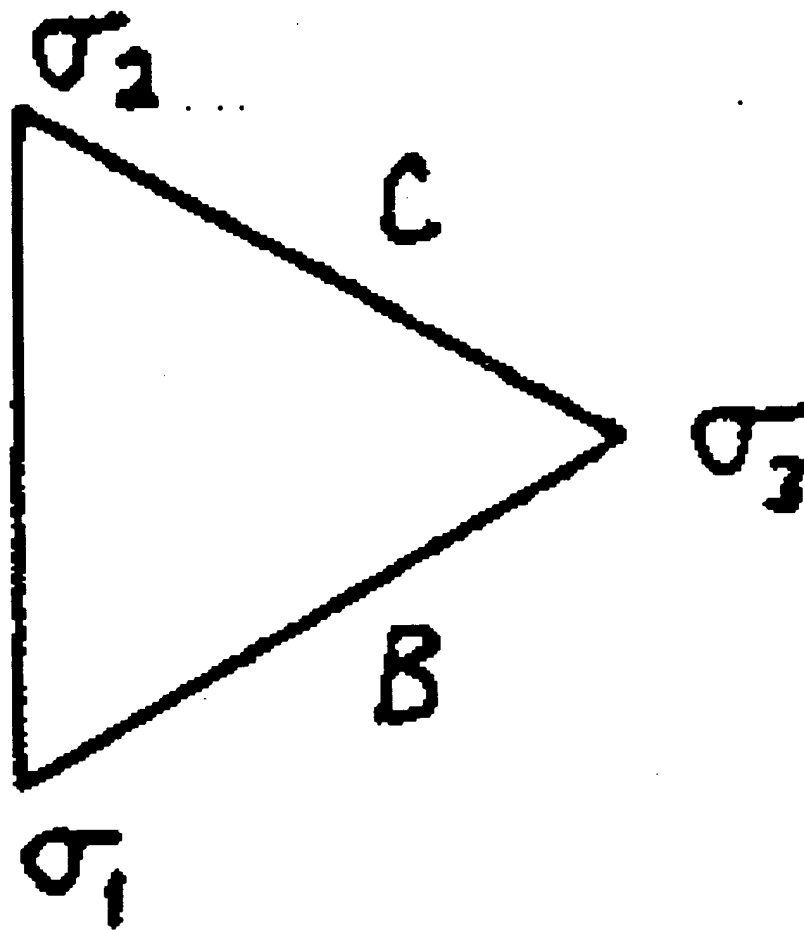
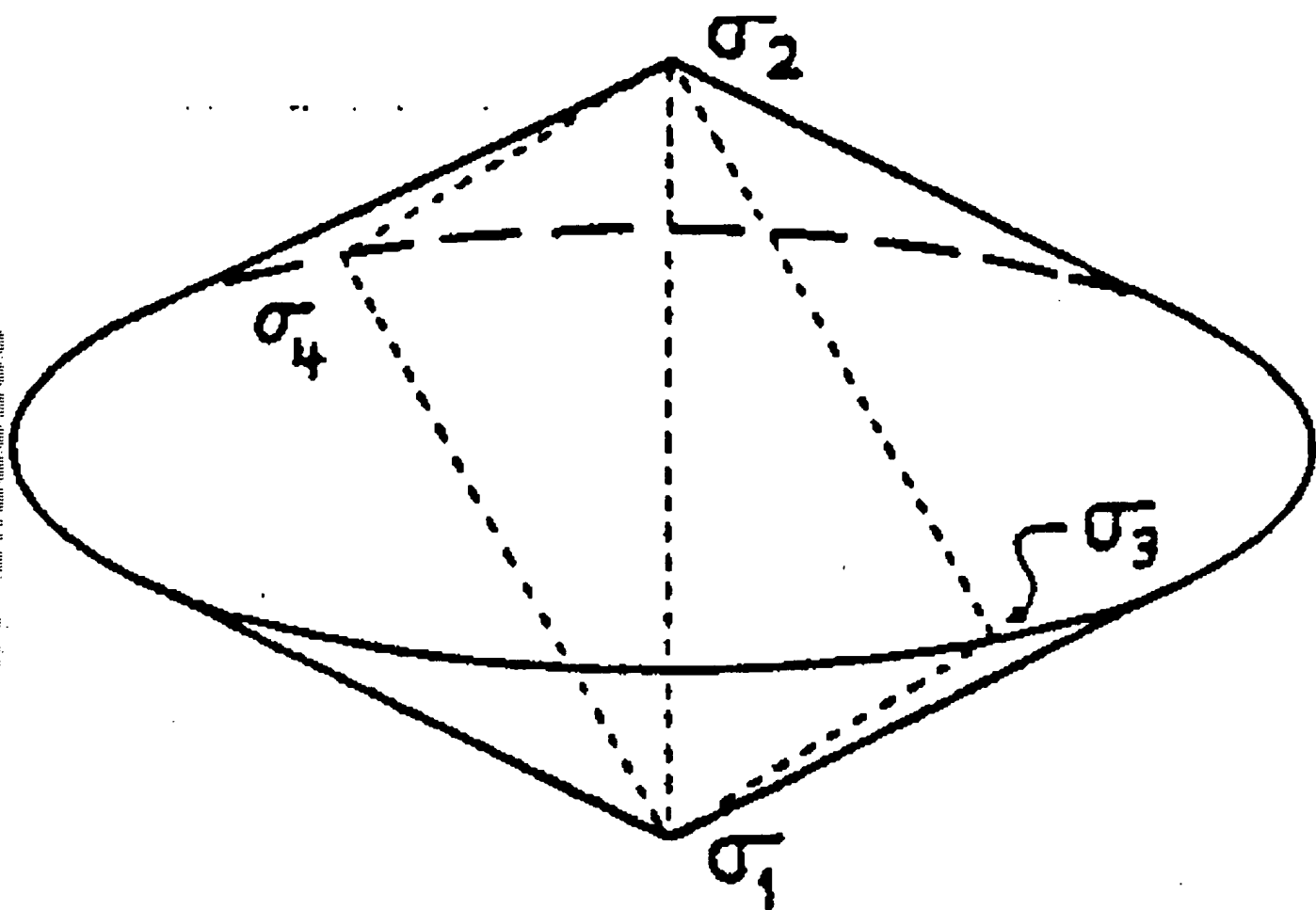


Fig 3



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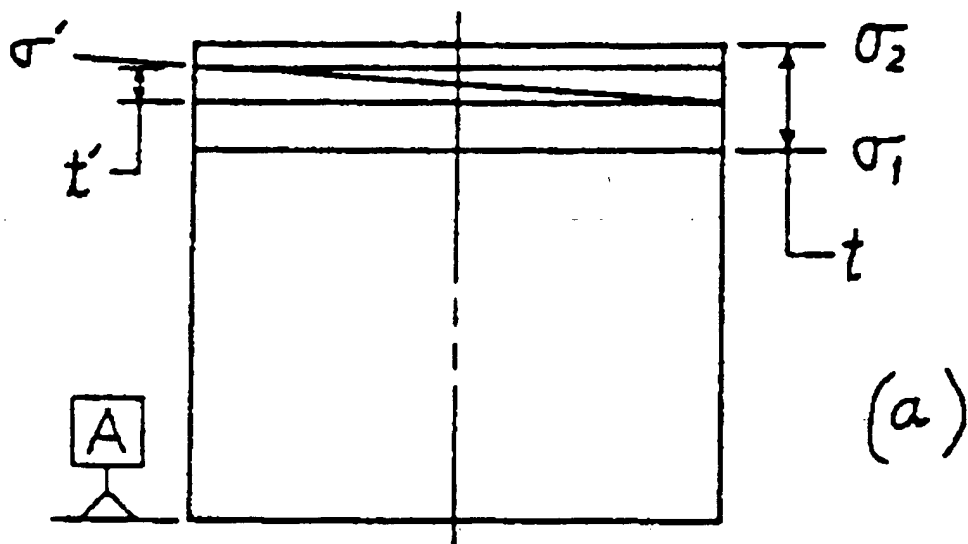


Fig 4a

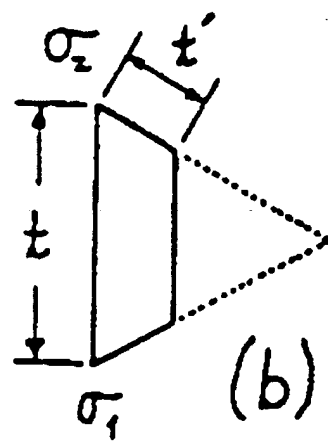


Fig 4b

(a)

Fig 5c

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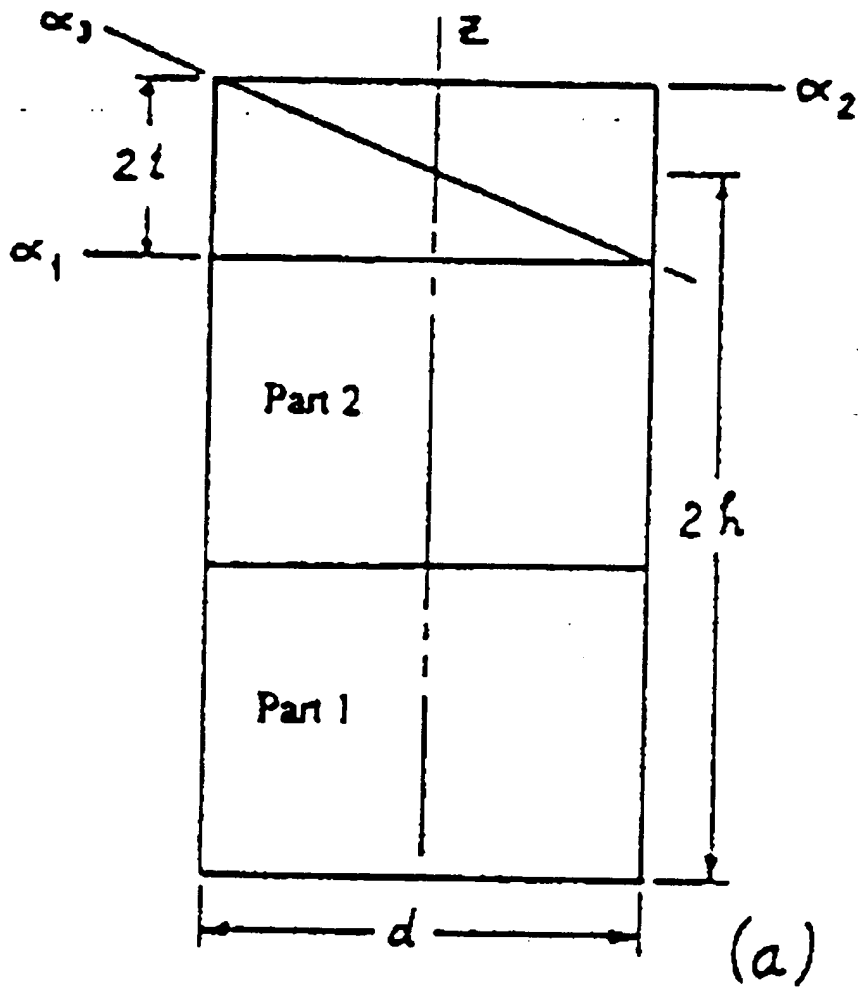


Fig 6a

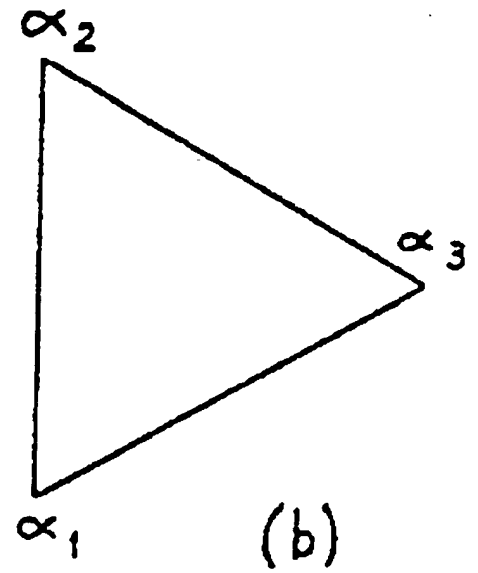
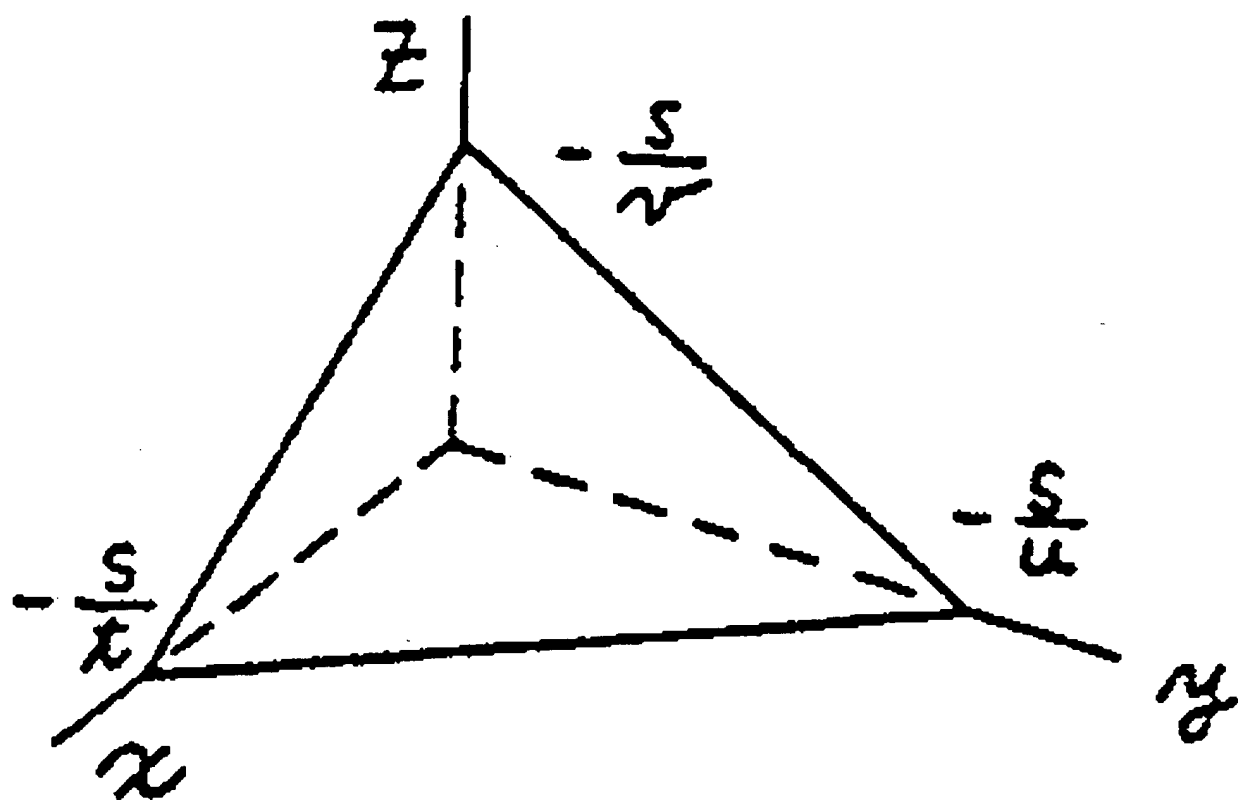


Fig 6b

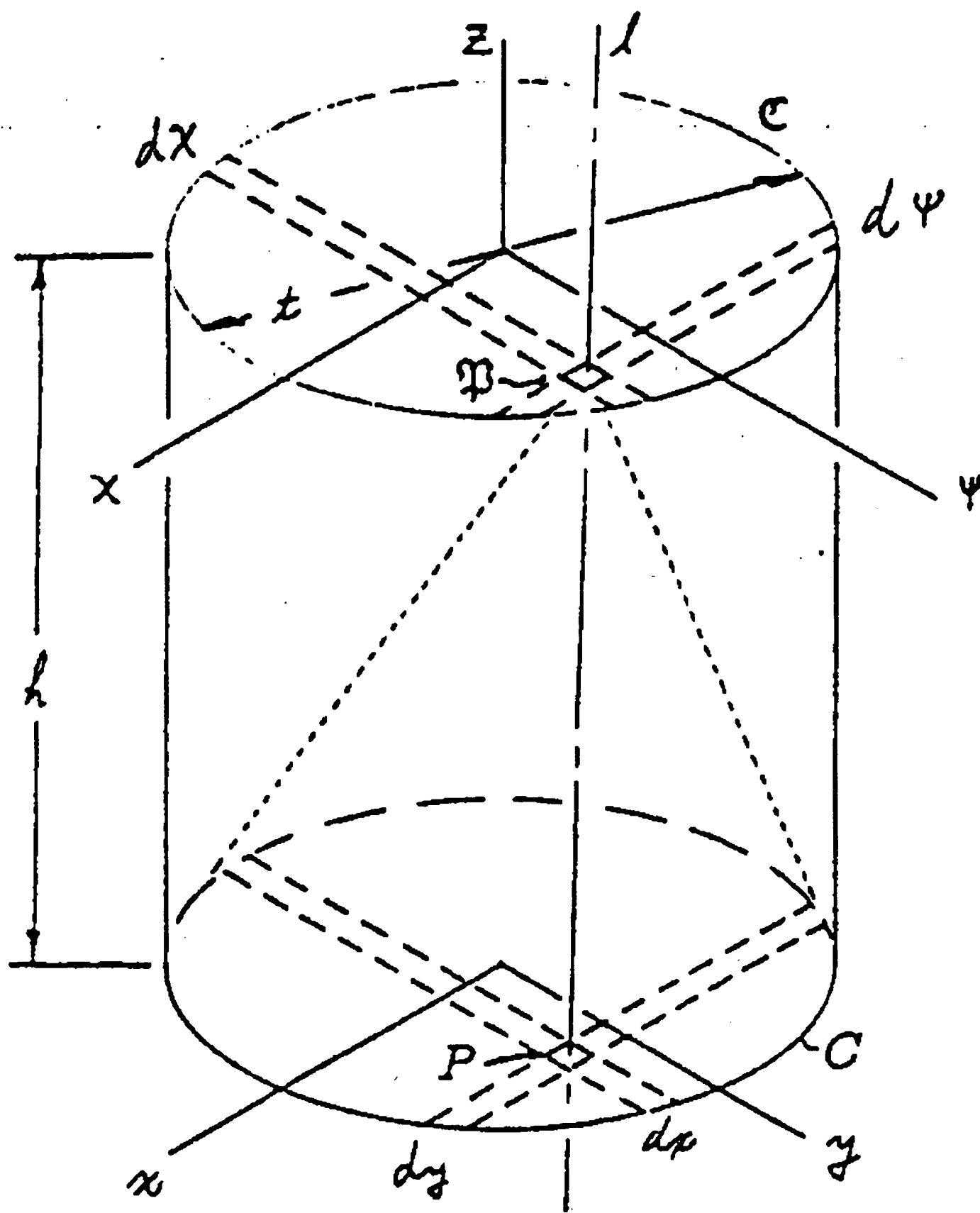
Fig 7



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Fig 8

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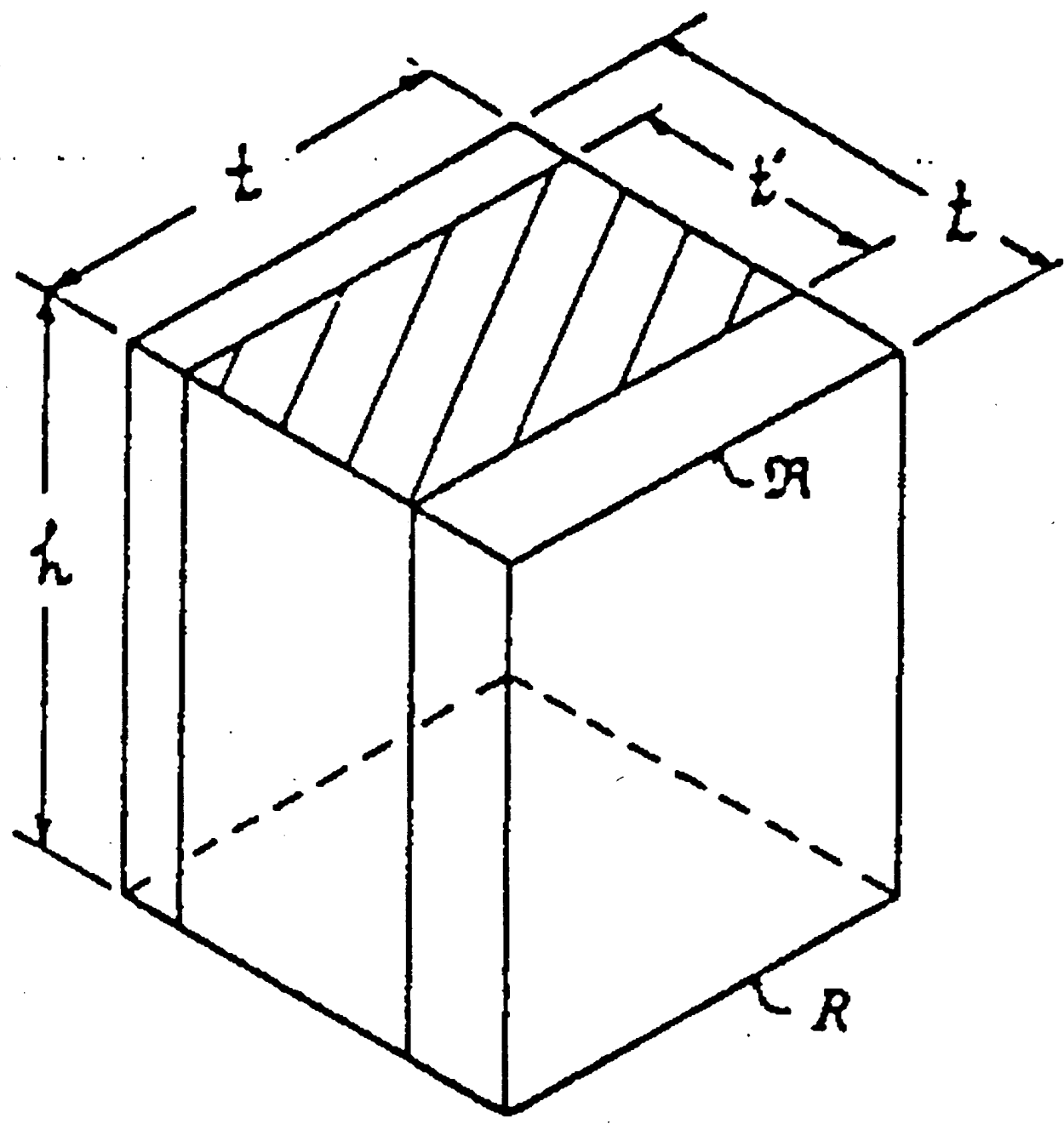
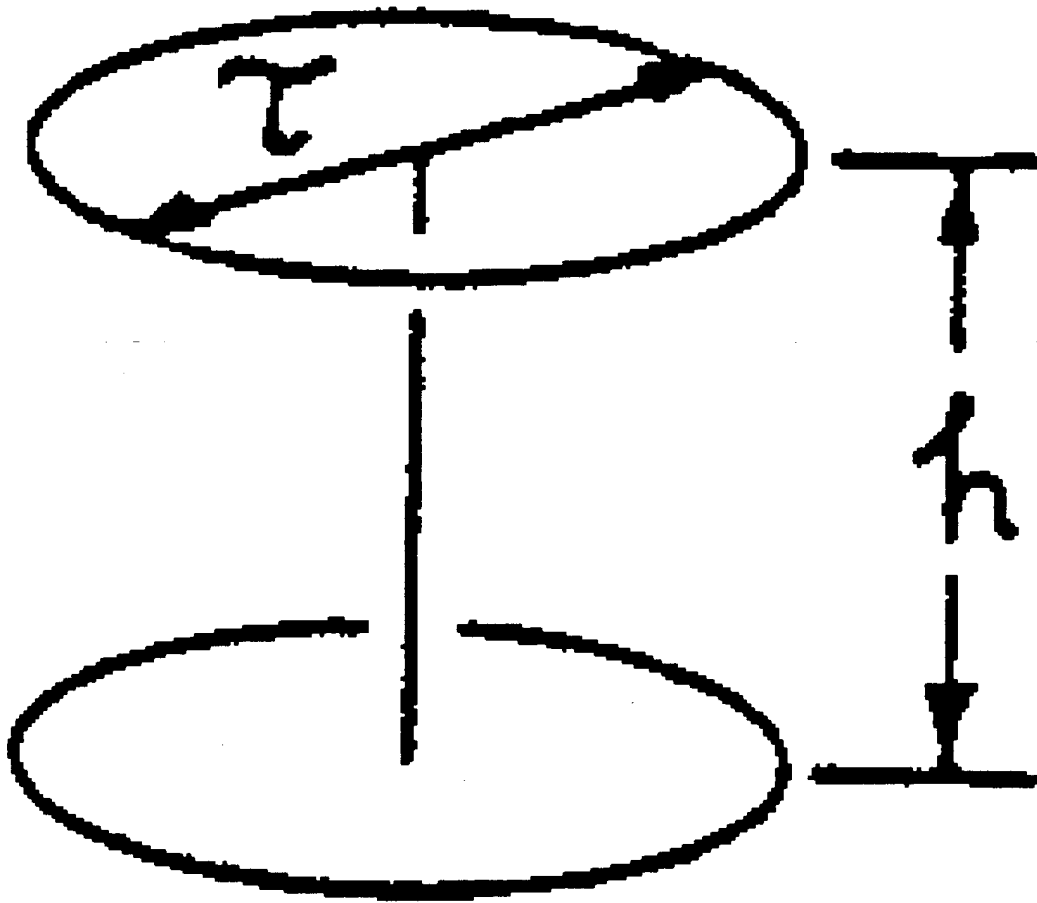


Fig 10



09507542-021800

Fig 11

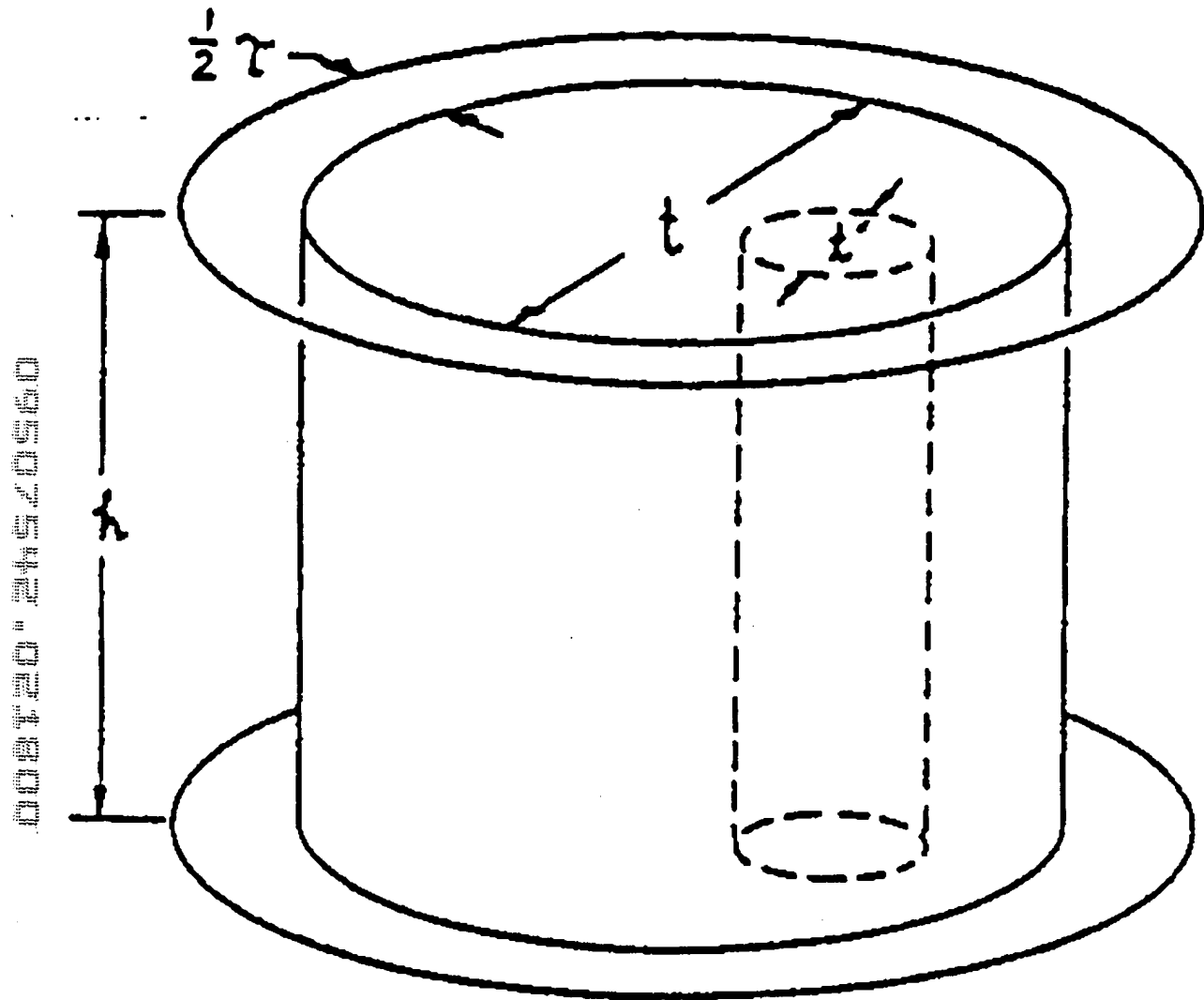


Fig 12a

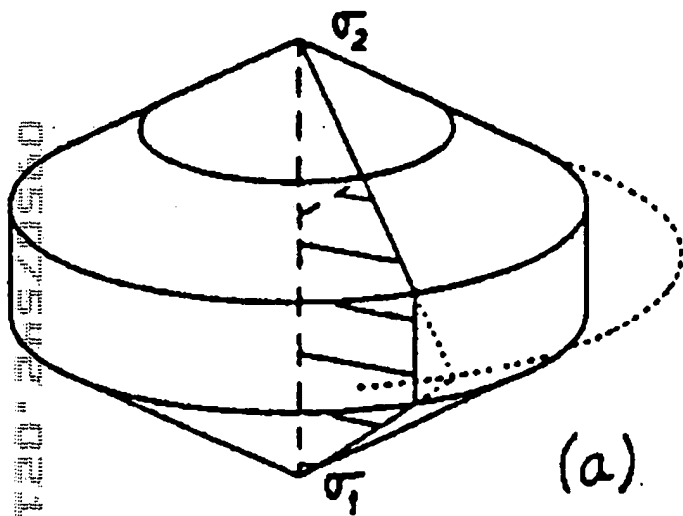
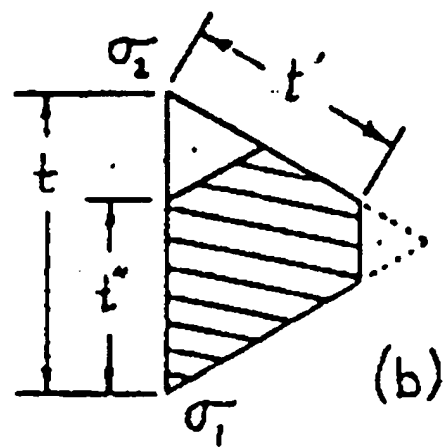


Fig 12b



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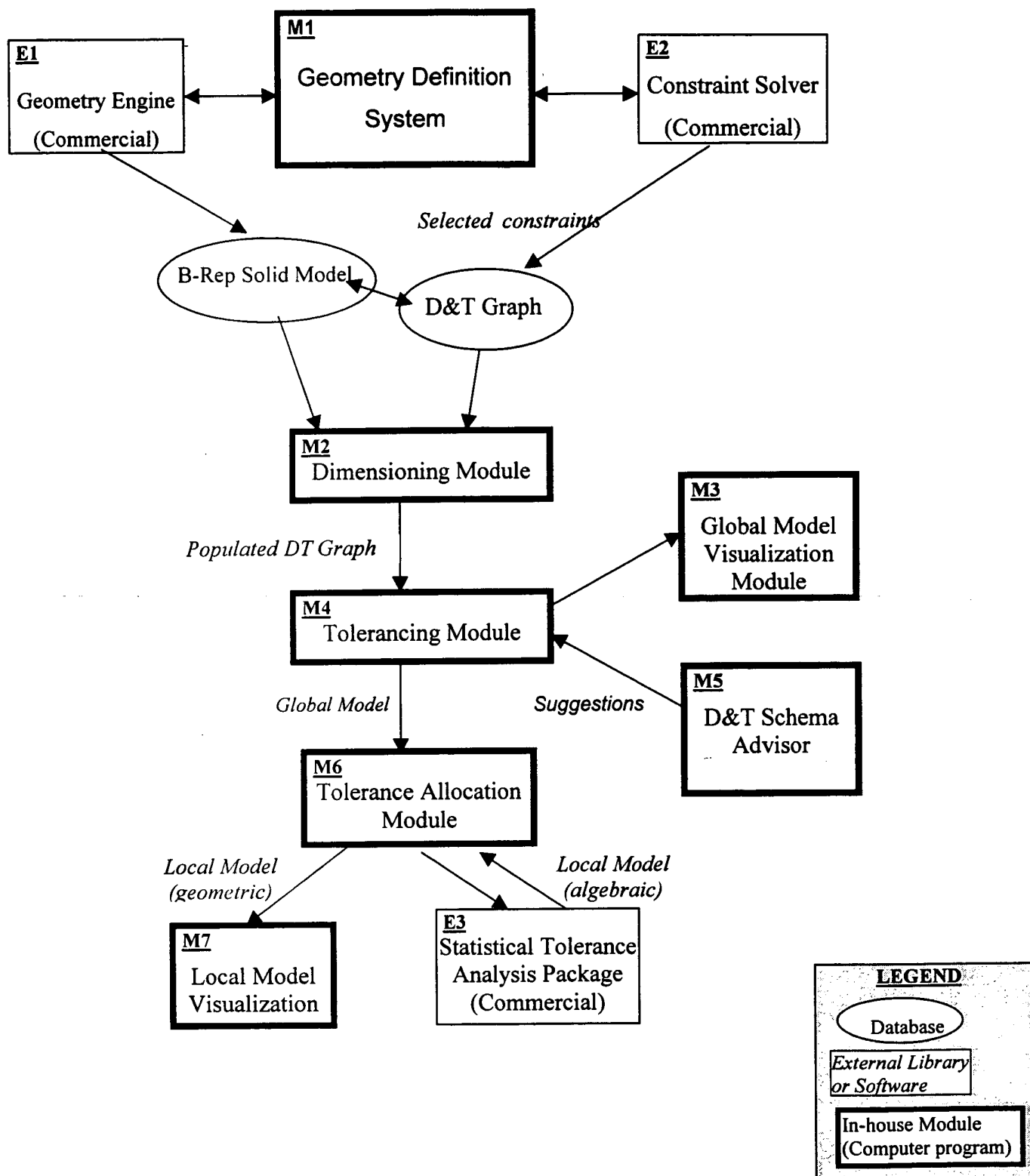


Figure 13 System Architecture

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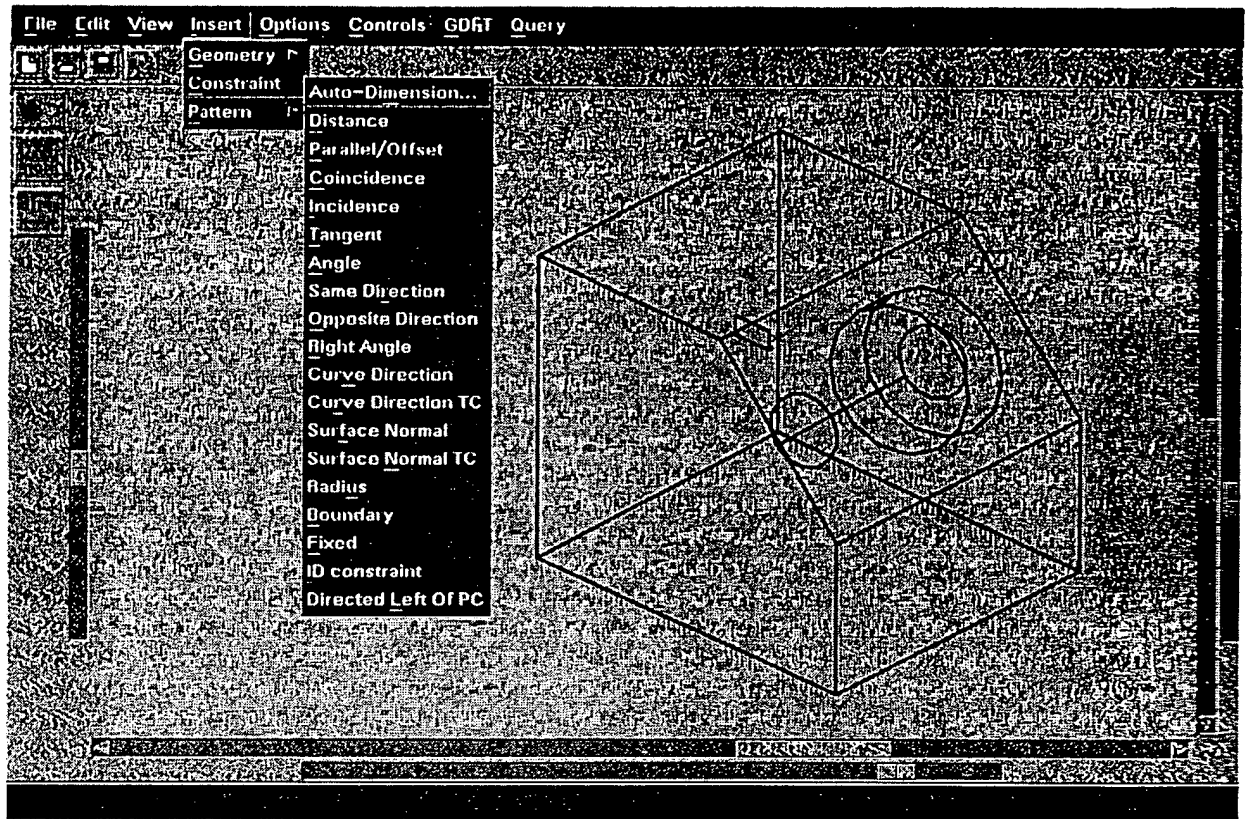


Figure 14

003720 245/0560

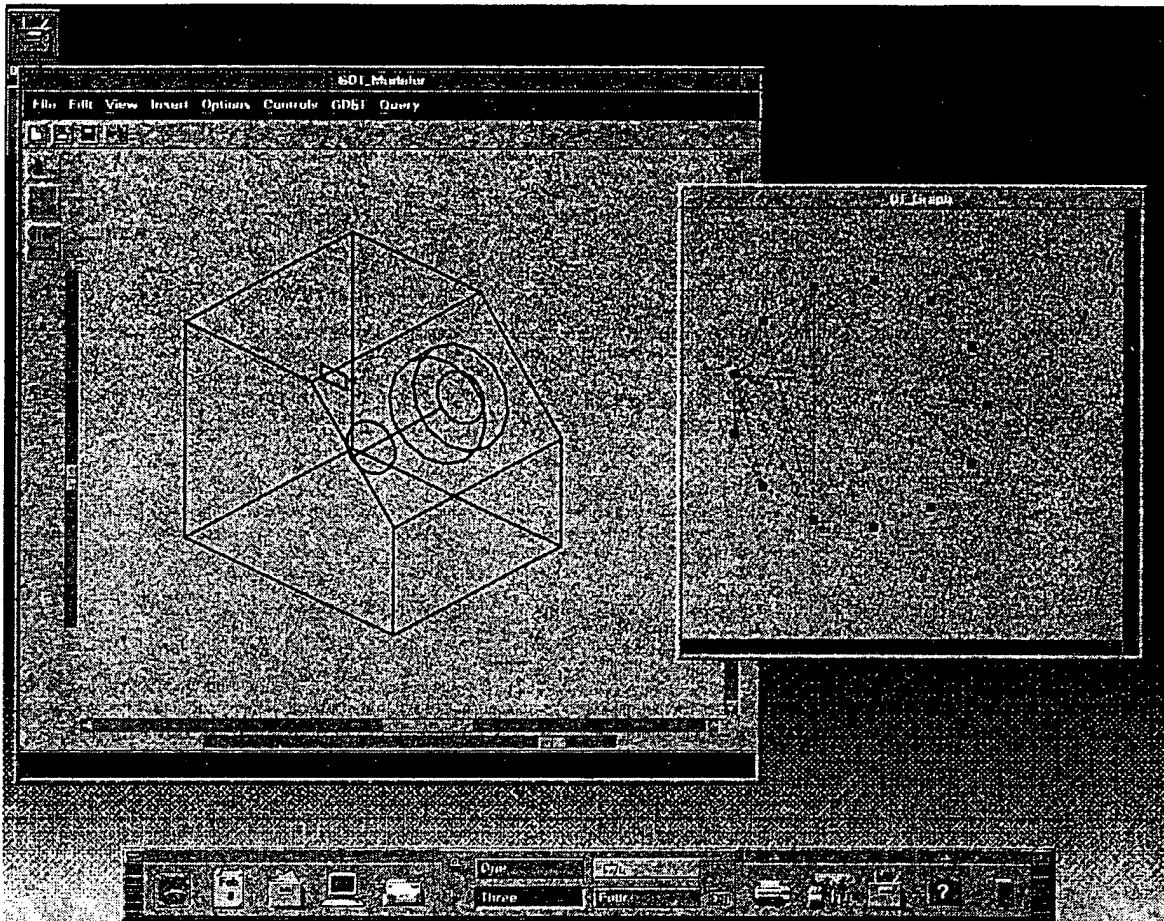


Figure 15

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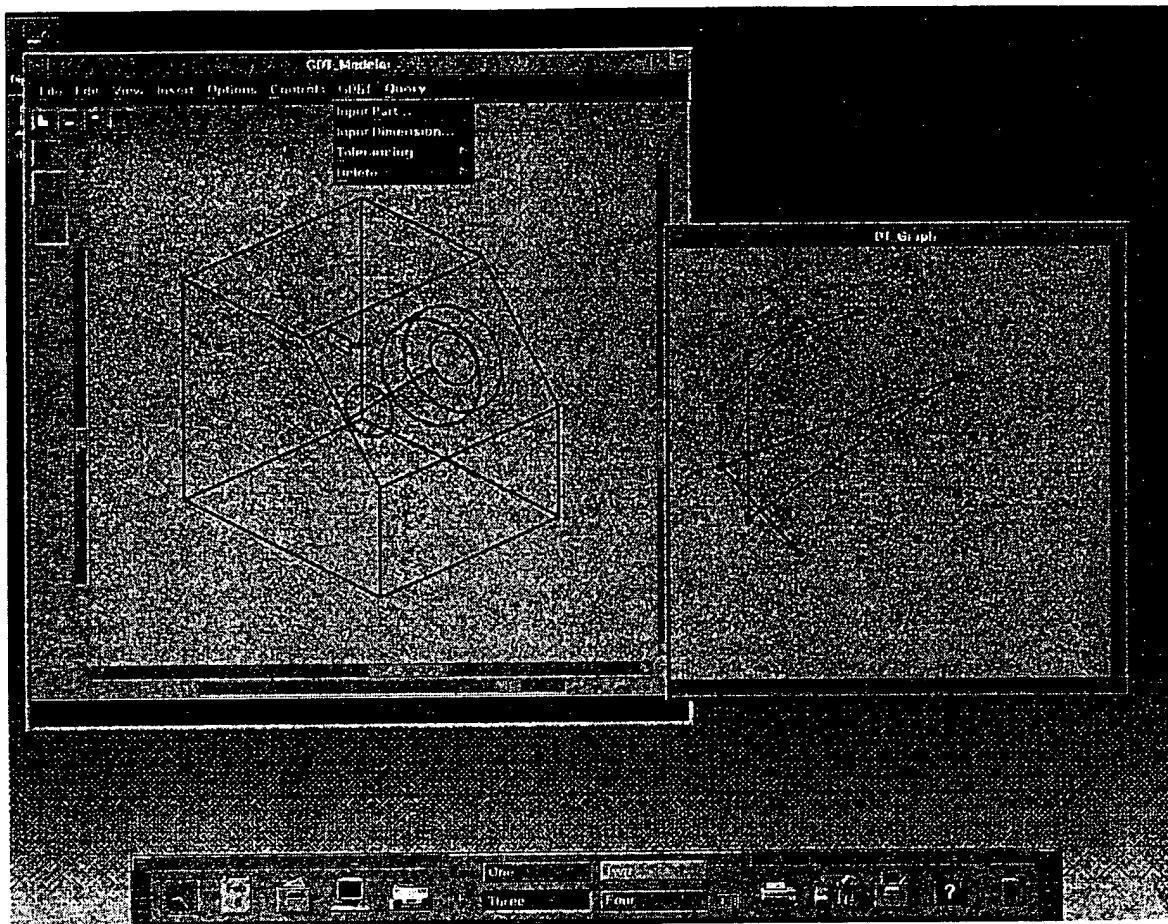


Figure 16

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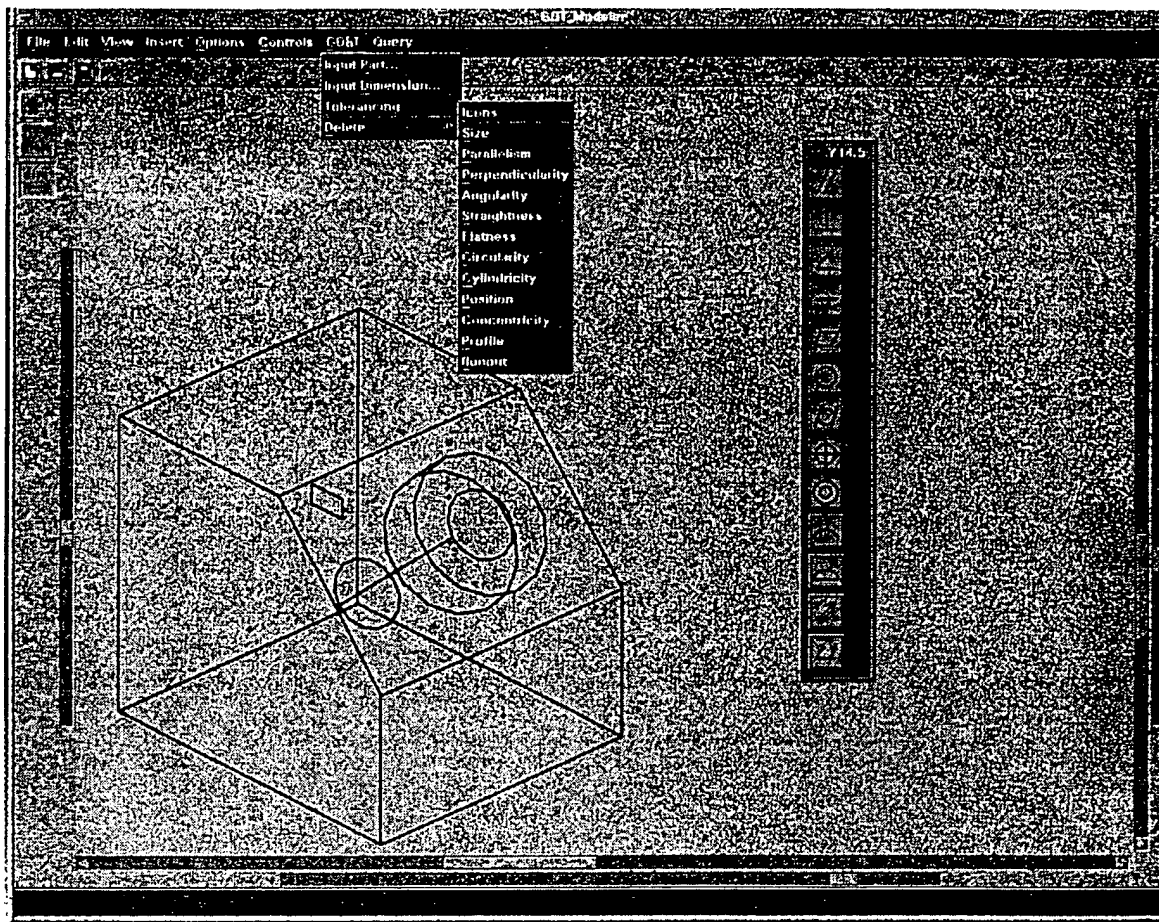


Figure 17

008120" 245/0560

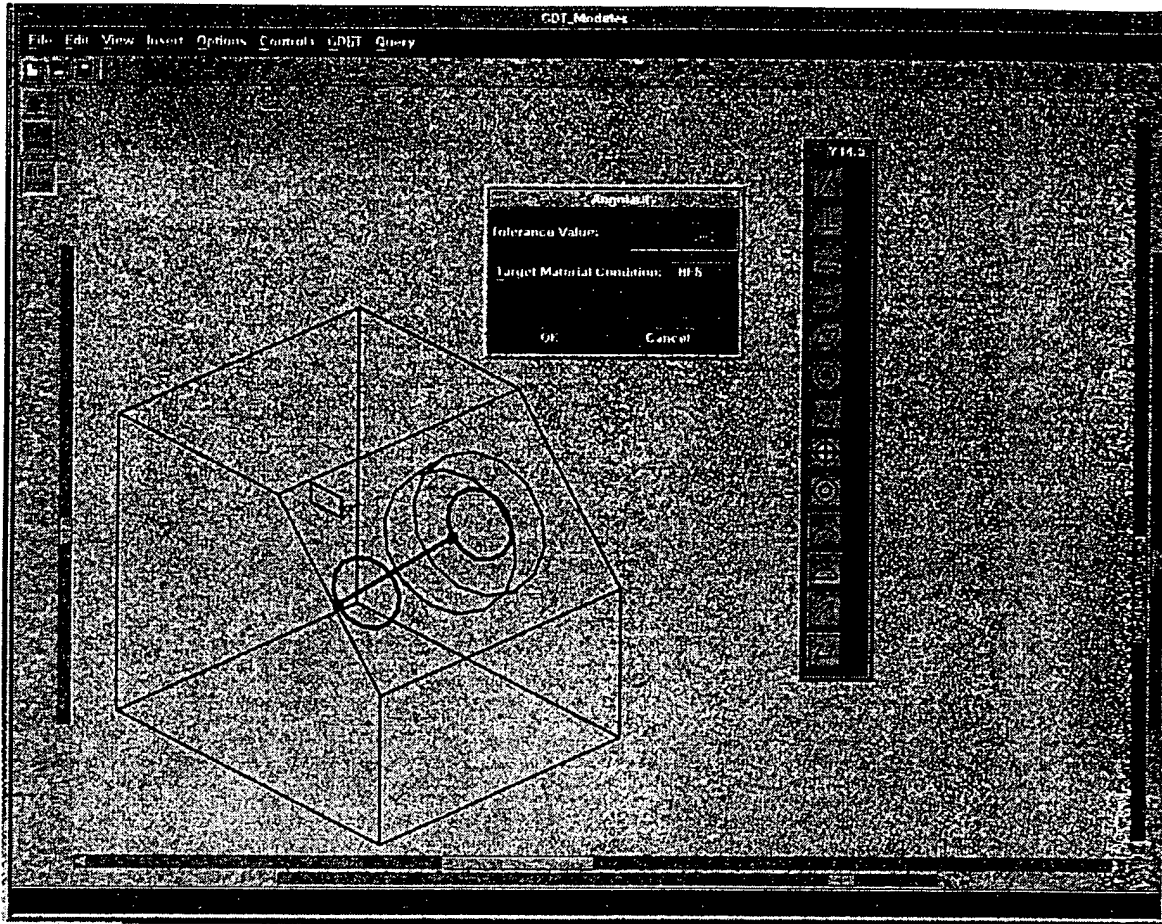


Figure 18

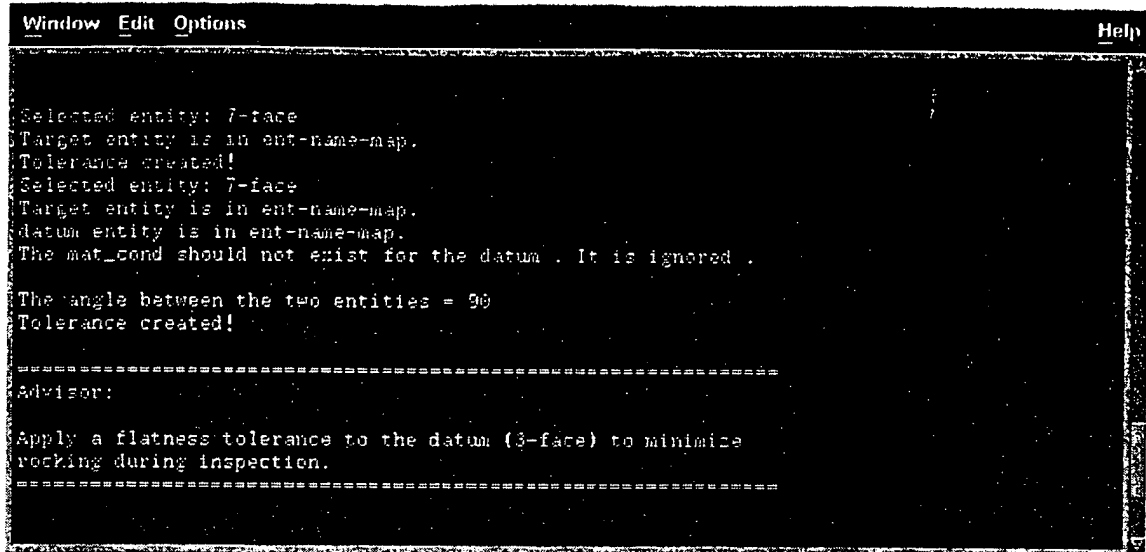


Figure 19

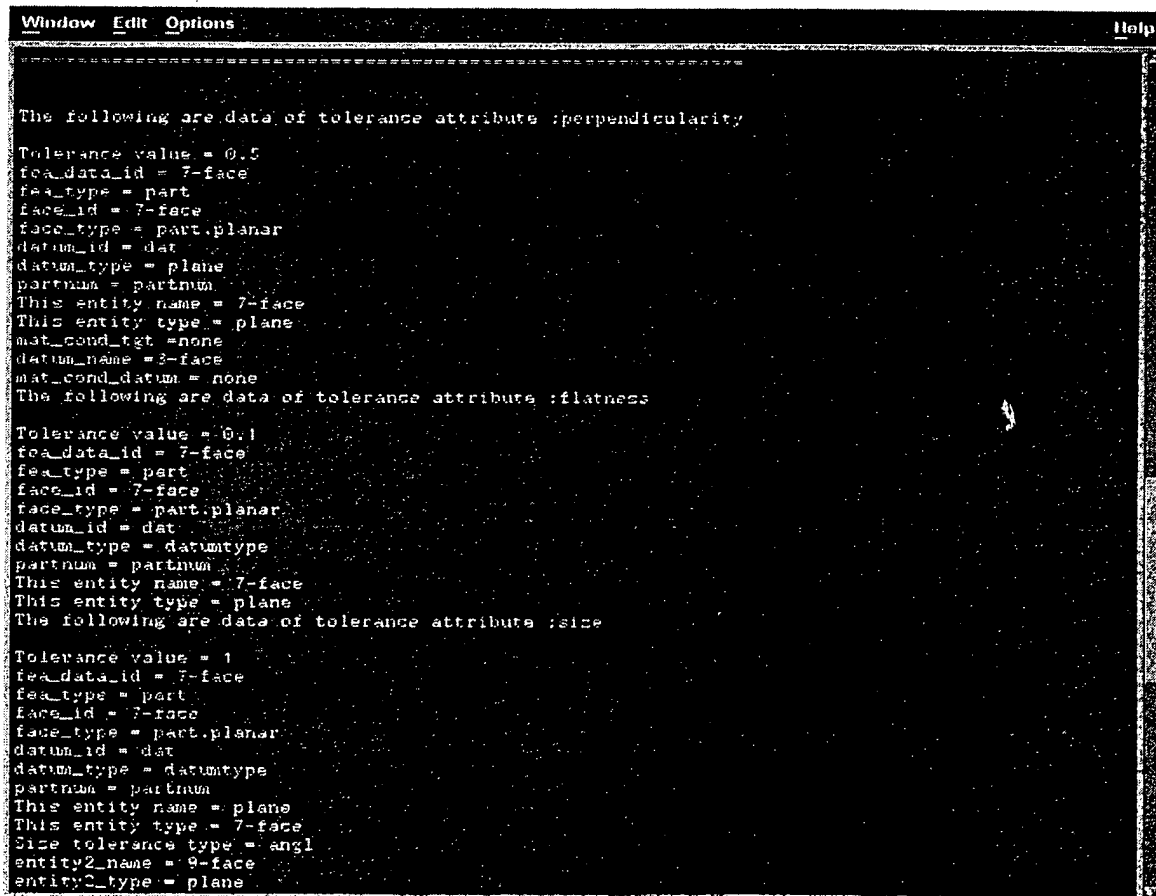


Figure 20

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Figure 21

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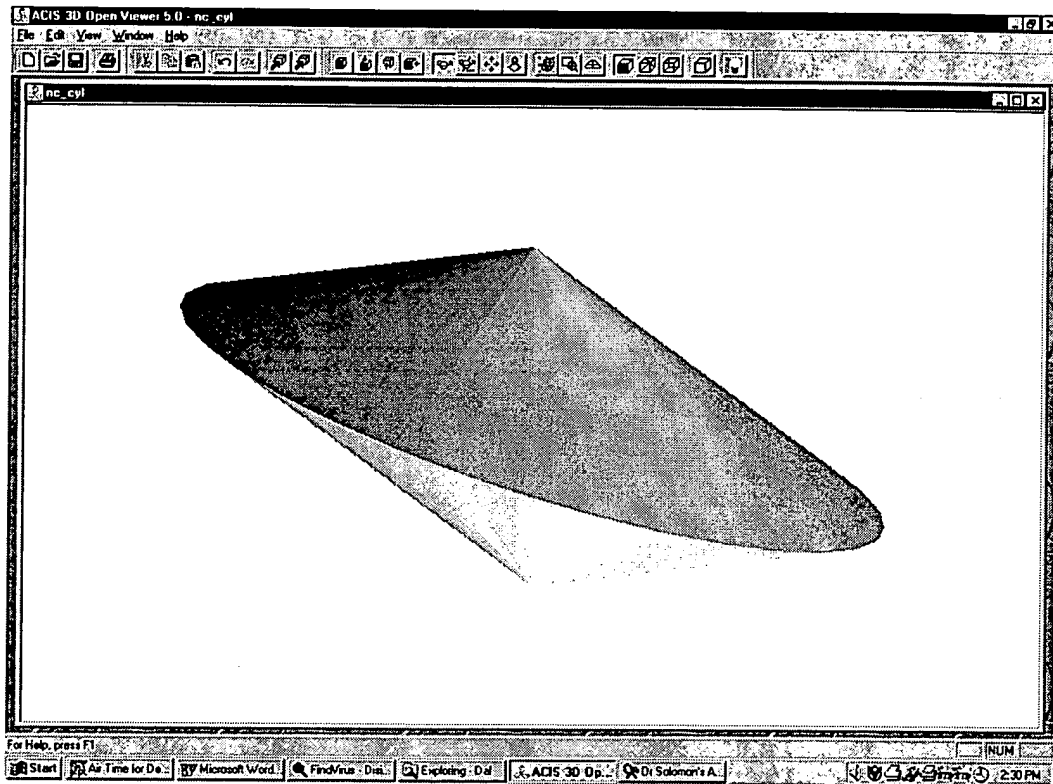


Figure 22

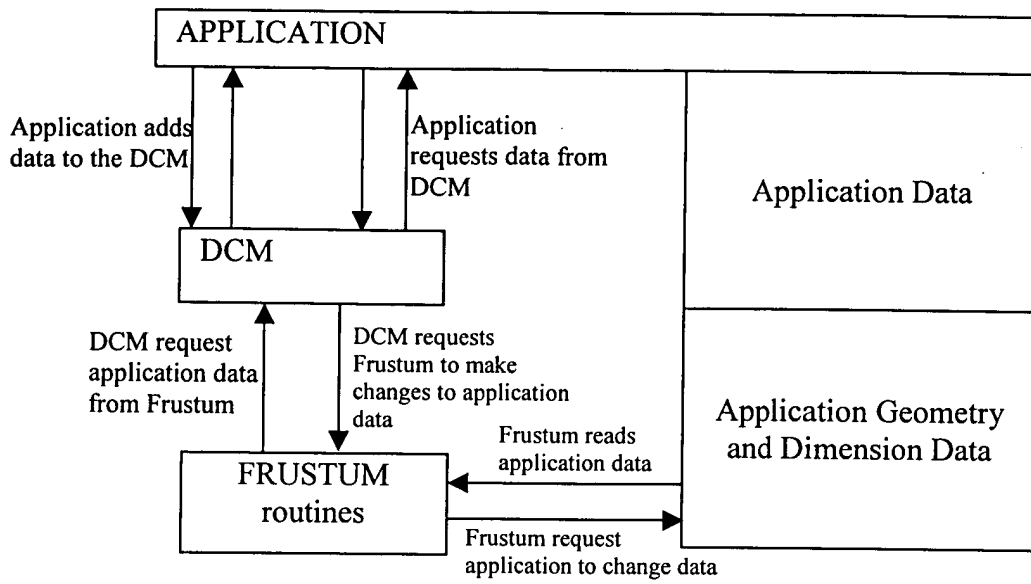


Figure 23

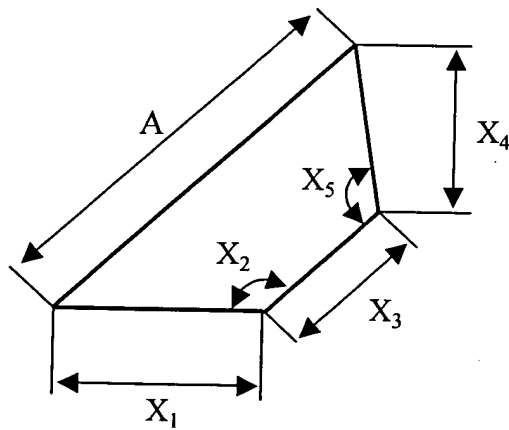


Figure 24

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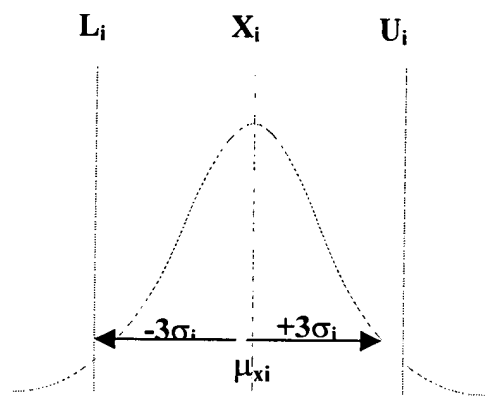


Figure 25

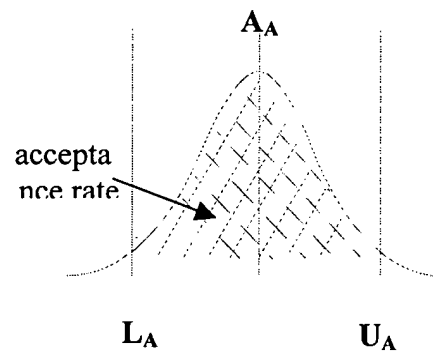


Figure 26